## **REMARKS**

Claims 1-98 are pending, with claims 17-50 and 67-98 withdrawn from consideration. Some of the claims and the specification have been amended to eliminate informalities. Elected claims 1-16 and 51-66 were all rejected under 35 U.S.C. § 102(e) a being anticipated by Sharrit et al. (U.S. Patent No. 5, 999,990).

Claims 17-50, which have been amended, are directed to a processor having a plurality of kernel planes with a plurality of kernels for processing data in a communication device. At least one kernel of the plurality of kernels has an interface adapted to receive and transmit information from the at least one kernel, a satellite kernel coupled to the interface, the satellite kernel performing a discrete class of operations within a communications application, and a local controller coupled to the interface and the satellite kernel and permitting the satellite kernel to operate autonomously with respect to the other of the plurality of kernels in the respective kernel plane. Claims 51-66 are directed to a computer readable medium containing therein computer readable codes that enable an electronic device to access the at least one kernel.

Sharrit et al. is directed to a communicator 10, which includes a plurality of reconfigurable resource units (RRUs) 13, a signal bus 14, a controller 16, a multiplexer 20, etc. The signal bus 14 is operative for transferring communications signals between various elements in the communicator 10. The plurality of RRUs 12 can each be dynamically altered to perform any of a plurality of processing tasks. The controller 16 determines a plurality of processing tasks to be supported by the communicator and configures the plurality of RRUs 12 accordingly. The multiplexer 30 selectively couples one or more of a plurality of antennas 22a-22n to the signal bus 14 in response to a control signal from the controller 16.

Sharrit does not teach, or even suggest, a kernel having a satellite kernel performing a discrete class of operations or a local controller that permits the kernel to operate autonomously with respect to other of a plurality of kernels, as required by the claims. Contrary to the Examiner's statement in the Office Action, Sarrit's RRU 12 is not equivalent to the claimed satellite kernel and Sarrit's controller 16 is not equivalent to the claimed local controller. The claimed satellite kernel

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and local controller are located at a level lower than the system level illustrated in Sarrit's Fig. 1. That is, assuming for the sake of argument that Sarrit's RRU 12 is equivalent to the claimed kernel, the claimed local controller and the satellite kernel would be located within the RRU 12. The kernel having a satellite kernel with its own local controller, as in the claimed invention, is advantageous over the system described in Sarrit because the kernel operates autonomously with respect to the balance of the kernels, and can therefore be activated or bypassed for a given function of an application, depending on the needs and protocol chosen for the application.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

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Respectfully submitted,

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